

**Performance of a novel Sn-Ag-Cu-Pt solder as interconnections in  
bi-facial solar cells under the sun**

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**ABSTRACT**

The performance of a solar panel is expected to be improved by using a low resistivity solder in the interconnection of bi-facial solar cells. Two solar panels were constructed using the popular Sn-3.8Ag-0.7Cu solder and a new Sn-3.8Ag-0.7Cu-0.2Pt composite solder as interconnection material. The power outputs of both solar panels under the sun were measured constantly throughout one week using an IV plotter and the total energy yields were calculated and compared. The total energy yield increases by 9.68% for the solar panel with the new Sn-Ag-Cu-Pt composite solder compared to that with the conventional Sn-Ag-Cu solder. This provides an alternative approach to boost the solar panel performance. Both solar panels have almost the same maximum power output throughout the week showing that the new solder is stable and shows no sign of degradation under the sun.

Keywords: Bi-facial solar cell, Composite solder, Interconnection, Power output

## 1.0 INTRODUCTION

Bi-facial solar cell, unlike ordinary solar cells, is able to absorb sunlight from both sides to generate electricity. A number of bi-facial solar cells are connected together by soldering to form a bi-facial solar panel.

Soldering is the most common and important way to connect the solar cell due to their excellent electrical conductivity, wetting behaviour and low melting temperature [1–5]. Therefore, solar panel interconnection materials are important due to their significant effects on energy production efficiency and reliability in real applications [6,7]. Previously, lead-based Sn-Pb (Tin-Lead) solder was widely used in electronic packaging due to some of its technical advantages, such as low melting point, outstanding wetting and spreading capabilities, competitive cost, satisfactory strength, ductility, and fatigue resistance [2,8–12]. However, the use of Sn-Pb was restricted due to environmental and toxicological concerns associated with the material [13]. As such, lead-free solder is the preferable choice in green technology.

Among the lead-free solders such as Sn-Ag (Tin-Silver), Sn-Cu (Tin-Copper), Sn-Zn (Tin-Zinc), Sn-Ag-Cu (Tin-Silver-Copper), etc., Sn-Ag-Cu was regarded as the most promising lead-free solder due to its good mechanical properties, acceptable wetting properties as well as suitable melting points [12,14–17]. The main issue impeding the successful application of all lead-free solders is the formation of excessive intermetallic compounds (IMC) between the solder and base material [8,18,19], resulting in various connectivity and reliability problems. Alloying elements such as Ni (Nickel), Ag (Silver), Pt (Platinum), Al (Aluminium), Co (Cobalt), Bi (Bismuth), rare earth elements, etc. had been added into the lead-free solders to enhance the properties of the interconnection. Among these, Pt possess several good properties such as low dissolution rate and resistance to oxidation [20,21]. The Sn-Ag-Cu-Pt solder was

developed to suppress excessive IMC growth. The objective of this study is to investigate the performance of Sn-Ag-Cu-Pt composite solder in bifacial solar cells under the actual operational condition.

In this study a newly developed Sn-Ag-Cu-Pt composite solder alloy was used in the interconnection of bi-facial solar cells. The interconnected solar cells that formed a bi-facial solar panel were placed under the sun to measure its performance and to calculate the contact resistance. The result was then compared with that of the bi-facial solar cells connected by conventional Sn-Ag-Cu solder.

## **2.0 MATERIALS AND METHODS**

Bi-facial solar cells (Figure 1) were purchased from PVG Solutions and connected to form two solar panels using the two types of solder, the novel Sn-3.8Ag-0.7Cu-0.2Pt composite solder and the conventional Sn-3.8Ag-0.7Cu solder, as shown in Figure 2. Agilent N3300A electronic load with two units of N3305A electronic load modules were used to carry out the solar panels electrical output measurement. The electronic load was controlled and programmed by an IV plotter program created in Agilent VEE programming environment. Flow chart for measurement of the power output is shown in Figure 3.

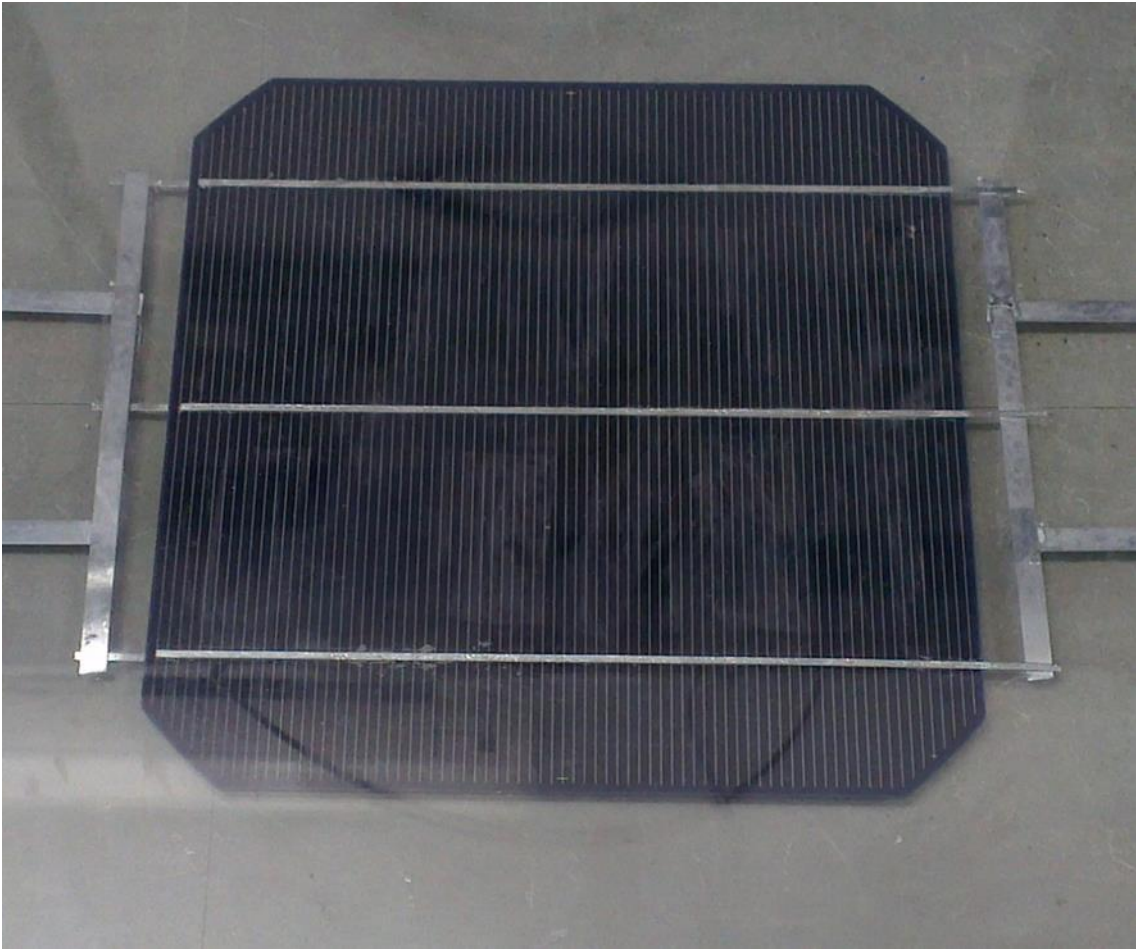


Figure 1: A bi-facial solar cell

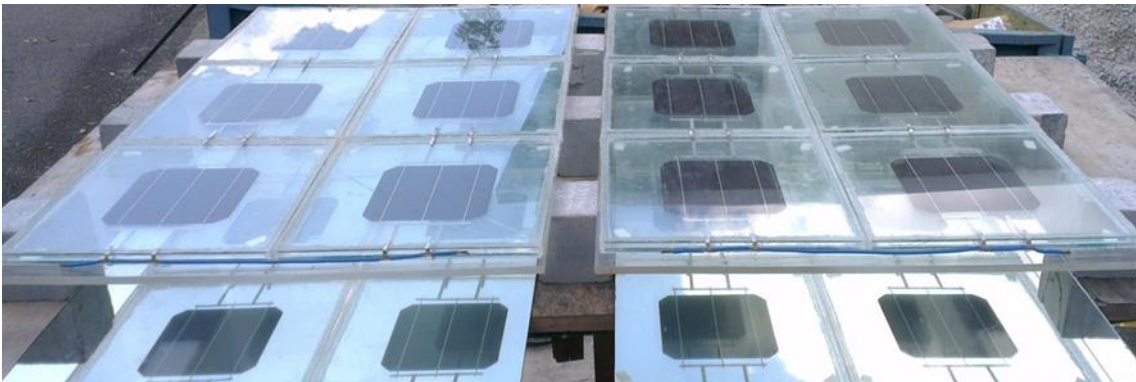


Figure 2: Bi-facial solar panels

Identical soldering process was used in the manufacturing process of the both solar panels using Sn-Ag-Cu solder and Sn-Ag-Cu-Pt solder. The solar panels were placed side by side under the sun at Universiti Tunku Abdul Rahman, Kuala Lumpur, Malaysia. The solar panel installation location has a mean temperature of 29°C and average humidity of 82% throughout the duration of the experiment. Both solar panels were connected to an electronic load for measurement of electrical output. An IV plotter program (Figure 4) was written for the electronic load to vary the resistive loads connected to the solar panels and measure both voltage and current simultaneously from both solar panels. The current-voltage graphs and power-voltage graphs were then plotted by the program to determine the power outputs from both solar panels. The electrical output measurement was taken at scheduled intervals between 10am to 4pm over a period of seven continuous non-rainy days.

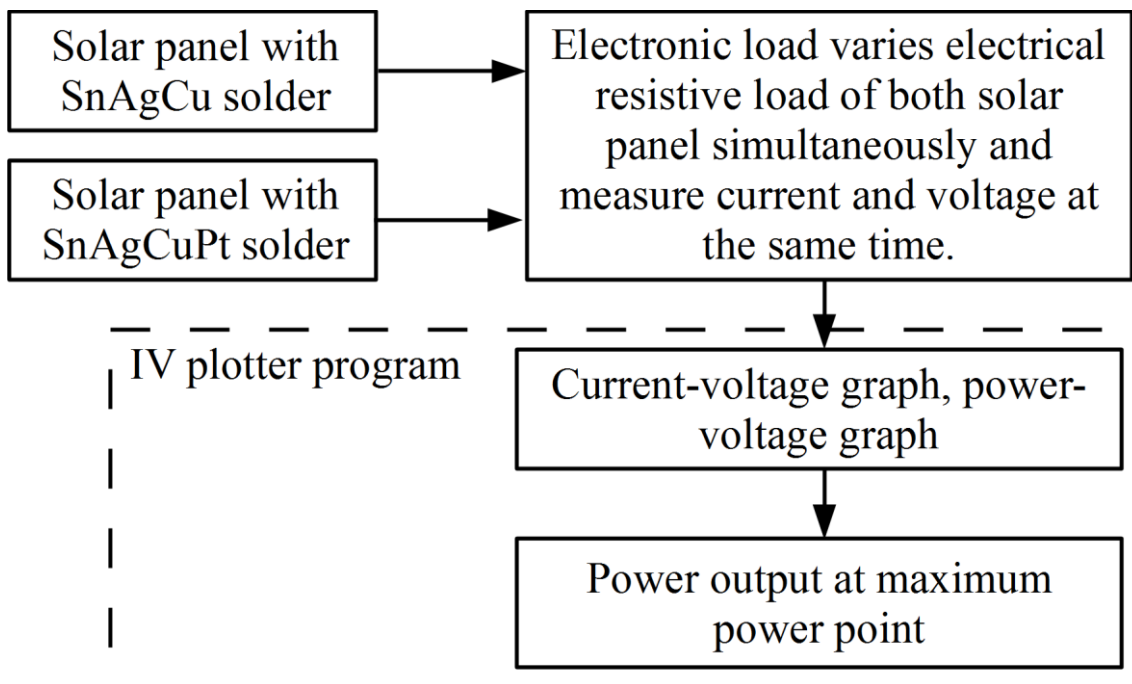


Figure 3: Flow chart of the power output measurement

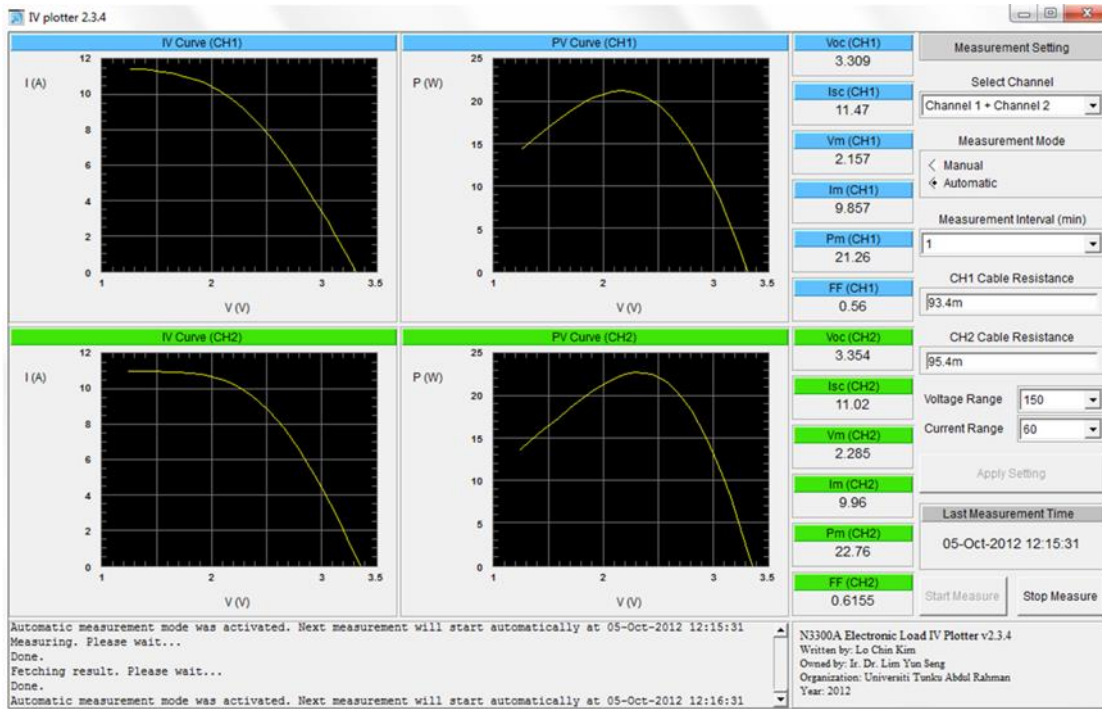


Figure 4: Screenshot of the IV plotter program

During the measurement, sunlight intensity varied from time to time. The electronic load was programmed to record the required voltage and current measurement from both solar panels simultaneously within one second. This ensures that the current-voltage and power-voltage graphs are plotted fast enough without being affected by the variation in sunlight intensity.

Power output values at maximum power points from both solar panels, taken throughout one day were used to calculate the daily energy yields from the solar panels. The daily energy yield from a solar panel was calculated by integrating the power output of a solar panel with respect to time throughout the day.

Besides, the current-voltage graph was curve-fitted using Equation (1) to extract the series resistance,  $R_s$ . Equation (1) is the circuit equation of the solar cell single diode model as shown in Figure 5.

$$I = I_{ph} - I_0 \left[ \exp \left( \frac{V + IR_s}{AV_T} \right) - 1 \right] - \frac{V + IR_s}{R_{sh}} \quad \text{- Equation (1)}$$

Where,

- I = Measured solar cell current (A);
- V = Measured solar cell voltage (V);
- $I_{ph}$  = Photogenerated current (A);
- $I_0$  = Reverse saturation current (A);
- $R_s$  = Series resistance ( $\Omega$ );
- $R_{sh}$  = Shunt resistance ( $\Omega$ );
- A = Diode ideality factor;
- $V_T$  = Thermal voltage (V) =  $kT/q$ ;
- k = Boltzmann constant (eV/K);
- T = Solar cell temperature (K);
- q = Elementary charge constant (C).

All the recorded current-voltage data throughout the seven-day period were curve-fitted in MATLAB. However, only those with good fit or  $R^2$  values equals or greater than 0.9999 were plotted into the result. The difference in series resistance from two solar panels is given by the difference in the solder resistance as both panels are identical except for the different types of solders used in each. Therefore, the change in solder resistance was represented by the difference of series resistances from the two solar panels. The increase in daily yield and decrease in solder resistance over the seven-day period can then be computed.

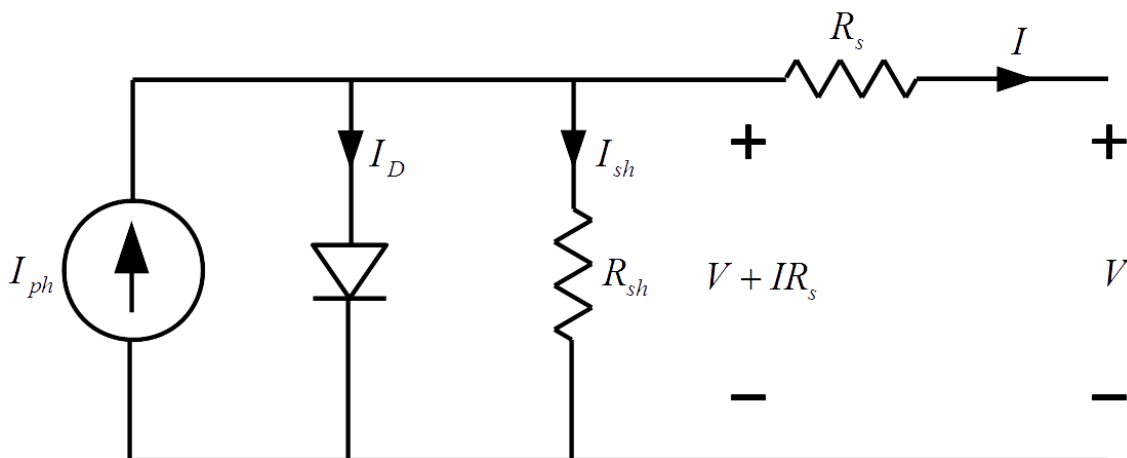


Figure 5: Single diode equivalent circuit

### 3.0 RESULTS

The graphs of power versus time and series resistance versus time are shown in Figure 6 and Figure 7 respectively. The total energy yield from solar panels with Sn-Ag-Cu solder and Sn-Ag-Cu-Pt solder are 0.4143kWh and 0.4544kWh respectively, i.e., there is an increase of 9.68% when the Sn-Ag-Cu-Pt solder was used as interconnection material. The peak power outputs for the solar panels (Figure 8) are: 15.21W – 18.28W for the Sn-Ag-Cu interconnection and 17.97W – 19.29W for the Sn-Ag-Cu-Pt interconnection. Therefore, there is no significant decrease observed in the peak power output. In addition, average series resistances were calculated to be 13.388m $\Omega$  and 10.740m $\Omega$  for solar panels with Sn-Ag-Cu interconnection and Sn-Ag-Cu-Pt interconnection respectively. Since both solar panels were manufactured using identical process and materials except for different solder as interconnection materials, the decrease in solder resistance equals to the decrease in series resistance, or 19.78%.



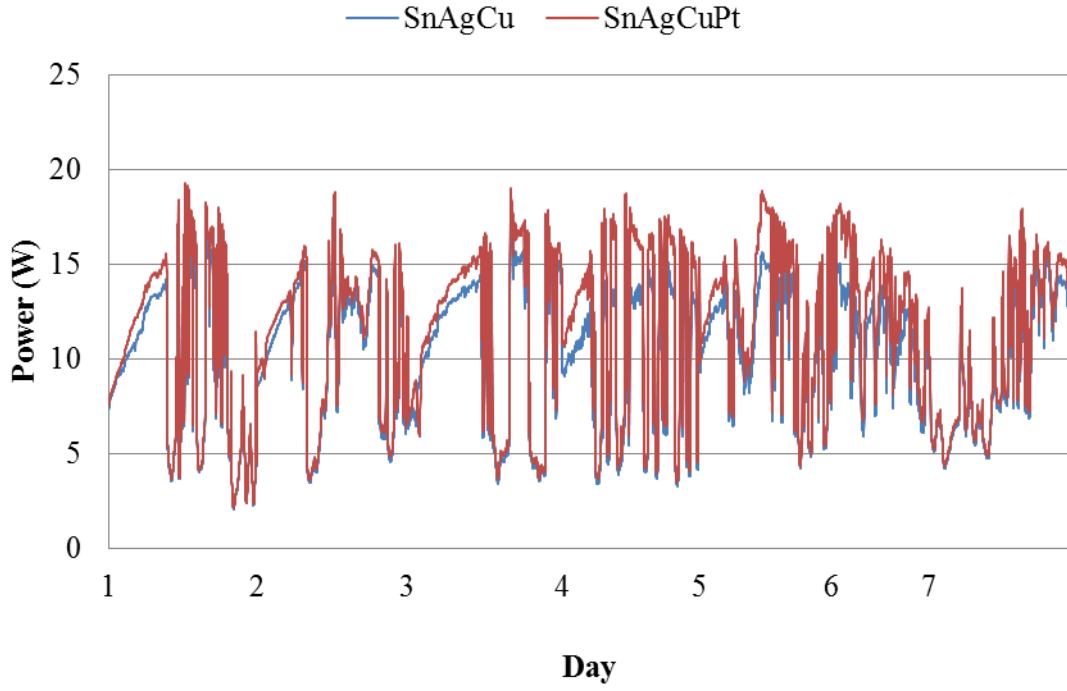


Figure 6: Power output of both solar panels versus time

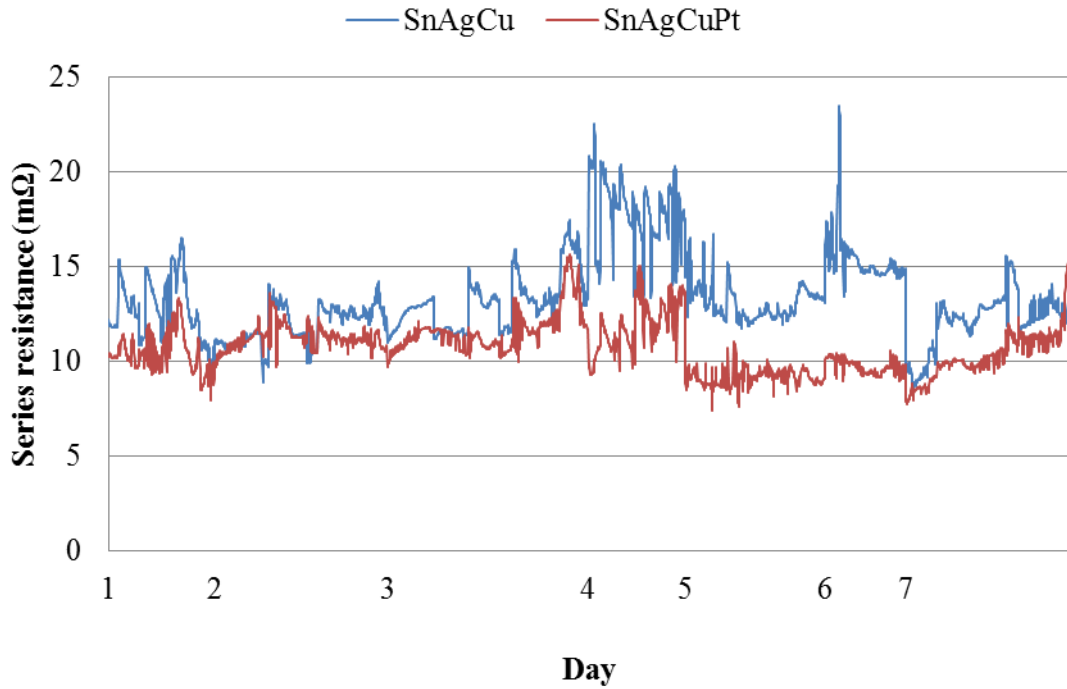


Figure 7: Series resistance of both solar panels versus time

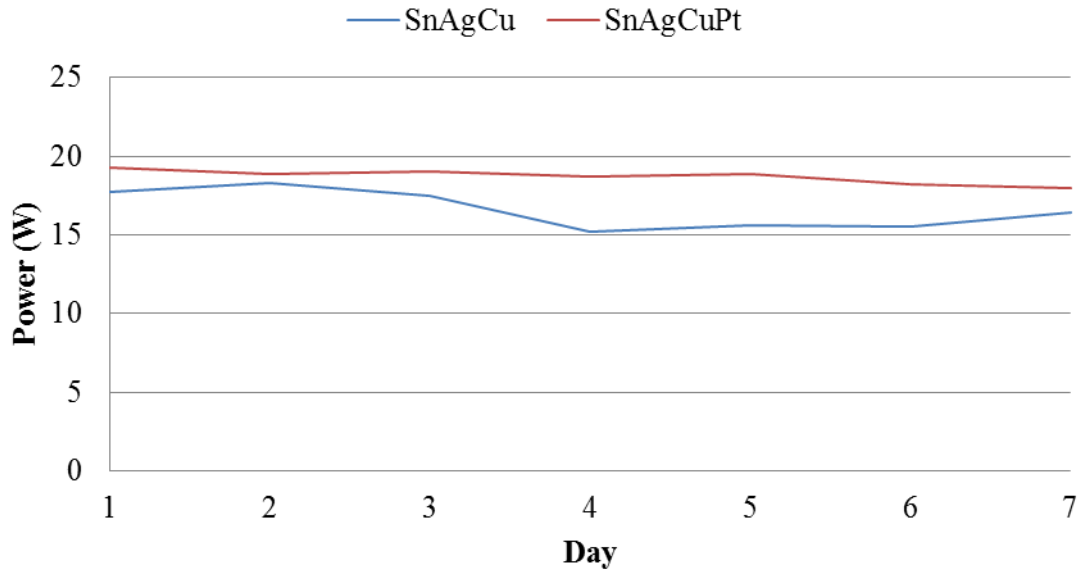


Figure 8: Peak power output throughout the experiment

The percentage of increase in daily energy yield and percentage of decrease in solder resistance are shown in Figure 9. A correlation was found between these percentages, (Figure 10), implying that the reduction in solder resistance results in the output of more energy in a day from the solar panel.

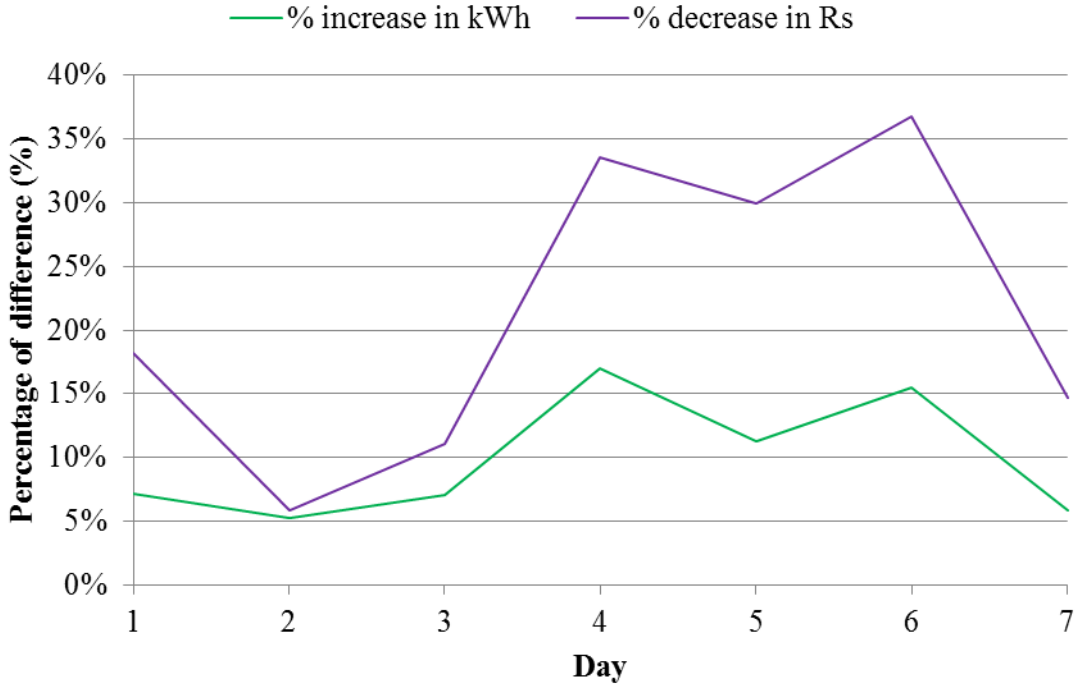


Figure 9: Percentage of increase in daily energy yield and percentage of decrease in solder resistance

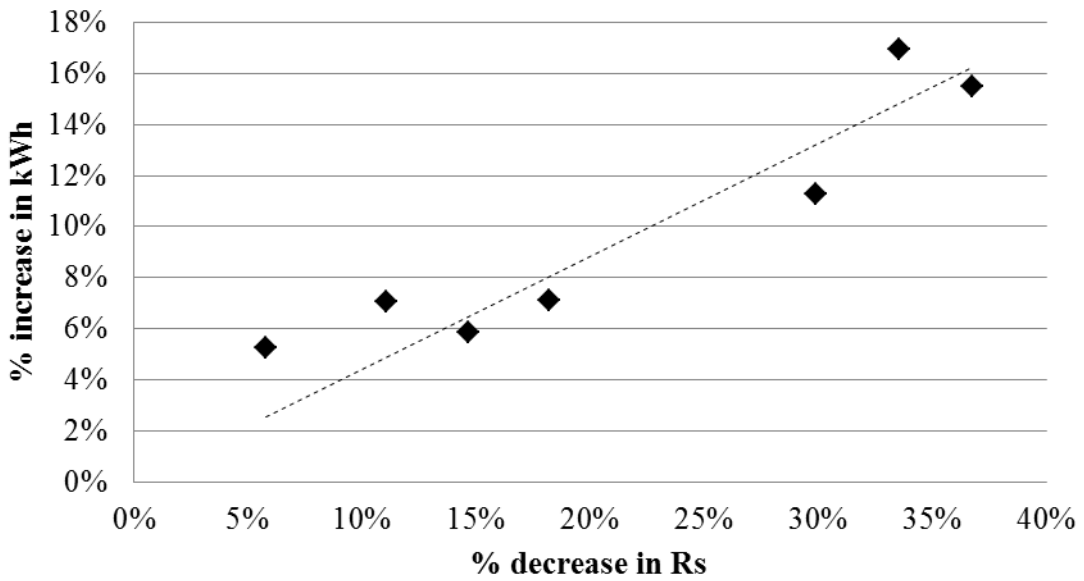


Figure 10: Correlation between the percentages of decrease in solder resistance and the percentage of increase in energy yield

The decrease in solder joint resistance by adding Pt into the Sn-Ag-Cu solder can be explained by comparing the cross-sectional microstructures for both the Sn-Ag-Cu and Sn-Ag-Cu-Pt interconnections (Figure 11 & Figure 12). In both solder joints,  $\text{Cu}_6\text{Sn}_5$  intermetallic compound (IMC) layer formed at the solder-copper ribbon interface while  $\text{Ag}_3\text{Sn}$  IMC layer formed at the solder- silver metallization interface.

At the Sn-Ag-Cu solder- copper ribbon interface, scallop-shaped  $\text{Cu}_6\text{Sn}_5$  IMCs makes up the IMC layer with several large and spiky IMCs on the layer; and block-like  $\text{Ag}_3\text{Sn}$  IMCs with sharp edges formed at the Sn-Ag-Cu solder- silver metallization interface. These sharp-edged IMCs are local stress concentrators which will initiate crack propagation in the solder joint and eventually affect the solder joint reliability. The voids present at the solder-silver metallization interface disrupt the electrical current flow and causes the increase in resistance of the solder joint.

In the Sn-Ag-Cu-Pt solder joint (Figure 12), scallop-shaped  $\text{Cu}_6\text{Sn}_5$  IMCs were evenly distributed along the solder-copper ribbon interface and larger, block-like  $\text{Ag}_3\text{Sn}$  IMCs were formed at the solder- silver metallization interface. With the presence of 0.20 wt.% of Pt in the Sn-Ag-Cu solder, the interfacial morphology of solder joint was altered through formation of an even, continuous IMC layer and reduction in the number of voids. This accounts for the reduction of electrical resistance, hence the increase in the power output of the solar panel.

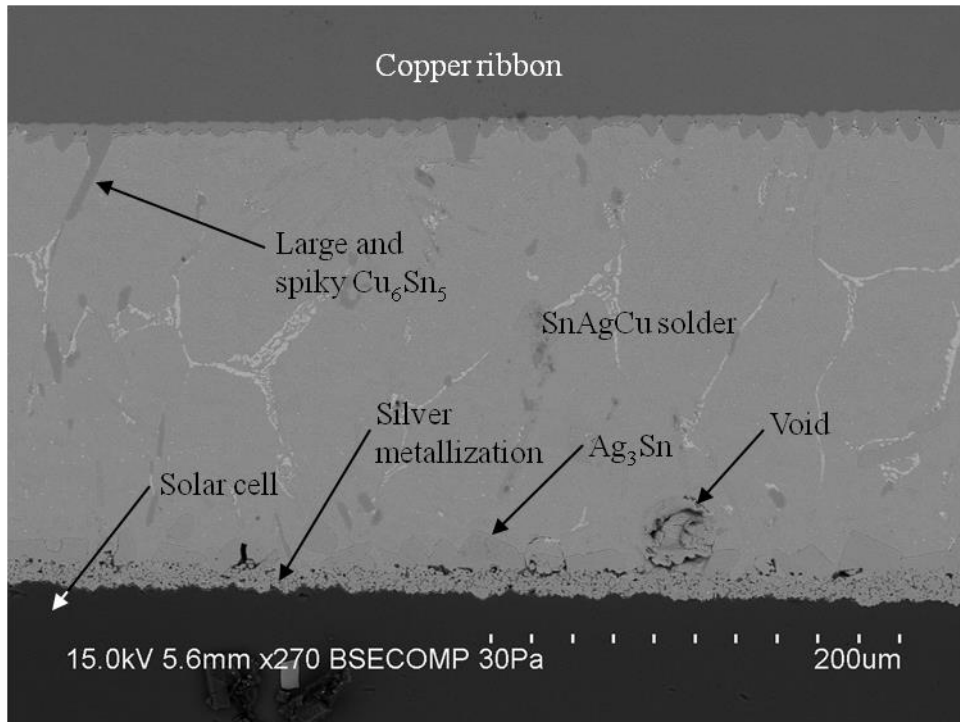


Figure 11: Micrograph of the Sn-Ag-Cu solder joint

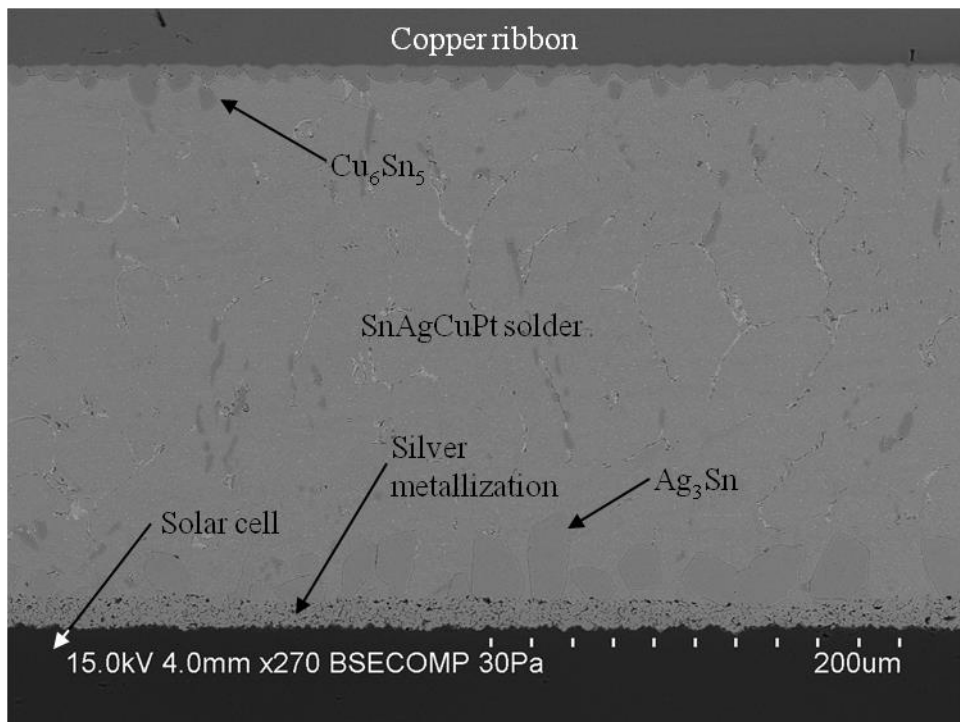


Figure 12: Micrograph of the Sn-Ag-Cu-Pt solder joint

#### 4.0 CONCLUSIONS

The novel Sn-Ag-Cu-Pt solder improved the bi-facial solar panel performance: total energy yield was increased by 9.68%. The increase in the solar panel yield energy is due to the reduction in solder resistance by 19.78%. Peak power outputs from the solar panels throughout a day were almost constant throughout the period of measurement, indicating that the novel Sn-Ag-Cu-Pt solder does not show any sign of degradation under the sun.

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